
JOINTLY ORGANIZED BY IEEE INDIA COUNCIL, CENTRE FOR EMBEDDED
PRODUCT DESIGN, CENTRE FOR ELECTRONICS DESIGN & TECHNOLOGY, NSIT

IN ASSOCIATION WITH IEEE CAS, BANGALORE CHAPTER



“ELECTRONICS MAKERS - 2017”

JULY 1-2, 2017

NETAJI SUBHAS INSTITUTE OF TECHNOLOGY, NEW DELHI

Call for Participation

Industry professionals as well as students are welcome to submit their work to this two-day conference, which aims at creating a platform for innovation and Do-It-Yourself electronics. It is an oft-quoted statistic that by 2020, India will spend more than \$400B in importing electronic technology/products. Do-It-Yourself and “Make-In-India” will surely help in bringing down this number and creating jobs in India in the core sectors of Electronics, Electrical, and Computer Engineering. Initiatives such as Open Source hardware and software as well as availability of low-cost semiconductor products are accelerating the growth of DIY. In this conference, the aim will be manifold - to discuss problems that DIYers tend to face, to provide networking opportunities for DIYers to help one another, and to provide a forum to showcase some of the recent innovations in Electronics DIY space. **Prizes will be provided to 3 best entries!**

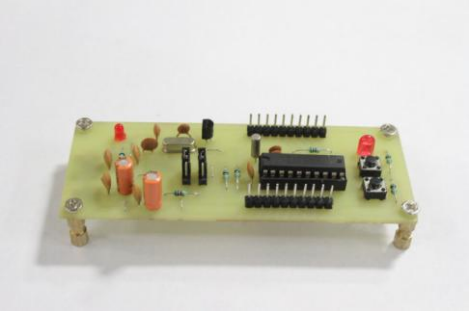
Registration

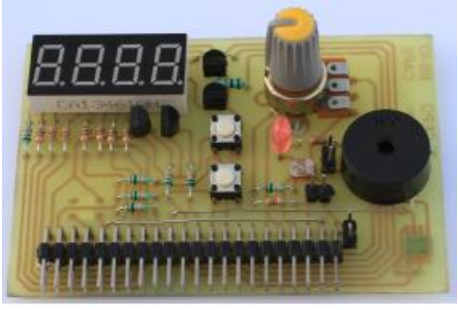
Prior registration is mandatory – please **FILL OUT FORM** no later than May 25. There is a participation fee to cover the costs of the conference. The fee structure is mentioned in the form. Please calculate the fee applicable to you and send a cheque or DD made out to ‘**Centre for Embedded Product Design, NSIT**’ and send it to Dr. Tarun Rawat, Electronics and Communication Engineering Division, Netaji Subhas Institute of Technology, Sector-3, Dwarka. New Delhi 110078 to reach no later than June 1. Your participation will be confirmed and further instructions will be sent through e-mail.

Submission of your Work

- The topic of the conference concerns innovative electronic system design. It does not include chip design.
- Accepted papers must be presented July 2 and the projects must be demonstrated. There is no scope for only paper presentation or only demo.
- DIY-ers who wish to present their work and demo their projects on Day 2 are requested to **FILL OUT FORM** no later than May 25. DIY-ers who wish to present their work must upload a PowerPoint presentation to ticepd@gmail.com with the subject line “Submission for EM2017”.
- Certificates from IEEE will be provided to authors of all accepted papers.
- It is OK to present/demo a project that has been discussed in other forums. No formal publication will be brought out and no copyright release will be sought. The presentation foils will be shared with the participants through Internet.
- The authors are expected to submit foils describing their projects before May 25, 2017.

Advance Technical Program

	Day 1-Track 1 DIY in India	Day 1 – Track 2 Hands-on Workshop
9.00am – 9.30am	Registration	Registration
9.30am- 10.00am	Inauguration	
10.00am-11.00am	Keynote Address by Amitabh Kant (Tentative)	<p style="text-align: center;">Hands-on Workshop “Microcontroller Learning Platform on a Shoestring Budget using MSP430 LunchBox” Each participant gets an MSP430 Lunchbox Kit.</p> 
11.00am-11.30am	Tea Break	
11.30am – 1.00pm	Panel Discussion #1: “Do It Yourself” to “Make in India”: A Leap of Faith.	
1.00pm – 2.00pm	Lunch and time for selected demos	
2.00pm – 4.00pm	Panel Discussion #2: Project based Engineering Education for “Make in India”	
4.00pm – 5.00pm	Tea and time for networking	

	Day 2-Track 1 Project Presentations	Day 2 – Track 2 Hands-on Workshop
09.00am – 9.30am	Registration	Registration
09.30am- 10.00am	Inauguration	
10.00am-11.00am	Project Presentations	<p style="text-align: center;">Hands-on Workshop Beaglebone and Linux - Open-Source Hardware and Software for DIY. Each participant gets a Beaglebone Cape.</p> 
11.00am-11.30am	Tea Break	
11.30am – 1.00pm	Project Presentations	
01.00pm – 2.00pm	Lunch	
02.00pm – 4.00pm	Project Demos	
04.00pm – 5.00pm	Tea and time for networking	