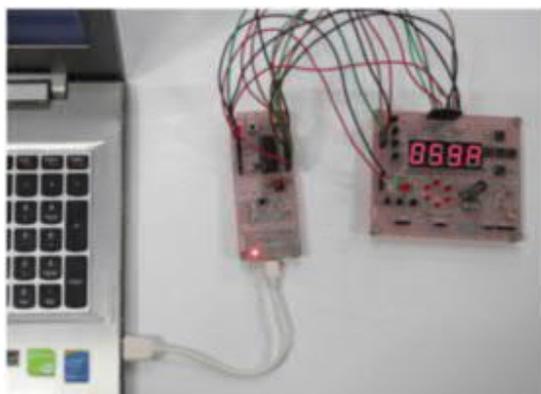# IEEE CAS Bangalore Chapter announces a 2-day workshop on
Electronics DIY – A Hands-on Approach

June 17-18, 2018 ... TI India Bangalore Campus, 560093 ... Instructor: Prof. Dhananjay V. Gadre (NSIT)

Are you interested in Electronics DIY projects? Do you have dreams of designing great products? All journeys begin with the first step. Attending this two-day workshop will launch you into the world of DIY, embedded processors, hardware, and embedded software.

Prof. Dhananjay Gadre, a well-known DIY-er and an experienced teacher who has trained a large number of DIY-ers, will guide you through the process of doing an electronics project. Learn about what are some best practices, what are some false starts, what are good and bad ideas. Also learn how to get started with a microcontroller-based project with a low-cost product called the MSP430 LunchBox. Incidentally, the "LunchBox" was a DIY project that was conceived and developed under the mentorship of Prof. Gadre. More than 50 experiments and small projects can be performed using the LunchBox when it is used with the Mini Voyager-1 system, which is also a product from Prof. Gadre's lab.

Each participant will receive an MSP430 LunchBox and a Mini-Launcher-1 to keep!

Who should attend?
The workshop is ideal for undergraduate students and postgraduate students, teachers from engineering colleges and working professionals who wish to dabble in DIY. It is expected that the students have taken a course on digital and analog electronics and are familiar with C programming and Internet tools. You should be a DIY person and must be willing to help yourself. There are a limited number of seats. Priority will be given to members of IEEE.

Registration
There is a nominal fee to cover the costs of the workshop. Participants will get a working lunch and refreshments on the days, a certificate, and an MSP430 LunchBox. You must first register online at https://goo.gl/forms/7OCFIFVerktLvnJA2 no later than May 25, 2018 and make the payment either through NEFT or by sending a DD using the below details. The DD/Chq should reach us by June 2, 2018.

Category	Students and Faculty	Industry Professionals
IEEE Members (you must provide proof of membership)	Rs 3000	Rs 4000
Non-Members	Rs 4000	Rs 5000

For Demand draft (DD): Name of the account / Payable to: IEEE CAS Bangalore Chapter
A/C number – 1057 2947 24
Central Bank of India, Miller Road Extension Branch (Code 02314)
For NEFT: IFSC Code: CBIN0282314
NOTE: Please send a screenshot of the NEFT or DD Transaction to accounts@ieee-cas-bangalore.org with a copy to registration@ieee-cas-bangalore.org mentioning your Name and A/c number for our reference. On the reverse side of DD please write your name and phone number.
The DD is to be sent to: "C.P. Ravikumar, Secretary, IEEE CAS Bangalore Chapter, Texas Instruments, Bagmane Tech Park, CV Raman Nagar, Bangalore 560093"

Agenda of the two-day workshop: Day-1

June 17, 8.30am – 9.30am	Registration
9.30am – 10.30am	The fascinating world of DIY
10.30am – 11.00am	Q&A and Brainstorming
11.00am – 11.30am	Tea Break
11.30am – 1.00pm	Getting started with the "LunchBox" and Mini-Voyager-1
1.00pm – 2.00pm	Lunch
2.00pm – 3.30pm	Digital I/O with Mini-Voyager-1
3.30pm – 4.00pm	Tea Break
4.00pm – 5.00pm	Timer and PWM with MSP430

Agenda of the two-day workshop: Day-2

June 18, 8.30am – 9.30am	Registration
9.30am – 10.30am	Interrupts with MSP430
10.30am – 11.00am	ADC using MSP430
11.00am – 11.30am	Tea Break
11.30am – 1.00pm	UART, SPI and I2C
1.00pm – 2.00pm	Lunch
2.00pm – 3.30pm	Low Power Modes and Projects
3.30pm – 4.00pm	Tea Break
4.00pm – 5.00pm	Experiences of Participants

About LunchBox and Mini-Voyager-1
https://dvgadre.blogspot.in/2018/05/a-diy-ecosystem-for-learning.html

About Prof. Gadre

Prof. Dhananjay V. Gadre (NSIT, New Delhi, India) is an avid DIY-er, a passionate teacher, and a renowned author completed his MSc (electronic science) from the University of Delhi and M.Engr (computer engineering) from the University of Idaho, USA. In his professional career of more than 28 years, he has taught at the SGTB Khalsa College, University of Delhi, worked as a scientific officer at the Inter University Centre for Astronomy and Astrophysics (IUCAA), Pune, and since 2001, has been with the Electronics and Communication Engineering Division, Netaji Subhas Institute of Technology (NSIT), New Delhi, currently as an associate professor. He directs two open access laboratories at NSIT, namely Centre for Electronics Design and Technology (CEDT) and TI Centre for Embedded Product Design (TI-CEPD).

Professor Gadre is the author of several professional articles and six books. One of his books has been translated into Chinese and another one into Greek. His recent book "TinyAVR Microcontroller Projects for the Evil Genius", published by McGraw Hill International consists of more than 30 hands-on projects and has been translated into Chinese and Russian. His latest book on TIVA ARM Cortex M4 microcontrollers published by Springer is just hot off the press! He is a licensed radio amateur with a call sign VU2NOX and hopes to design and build an amateur radio satellite in the near future.