

IEEE WINTECHCON 2019

Technical Conference by Women Engineers

Date: 27th September 2019

Venue: Scarlet Hall, Sheraton Grand, Whitefield, Bengaluru

Objective: Providing a platform for women technology leaders in India to lead and grow in emerging knowledge areas

Theme: **Future Technologies for a Smart, Safe and Sustainable World**



Organized by: IEEE CAS Bangalore Chapter in cooperation with IEEE Bangalore Section and IEEE WIE Council (IEEE Women in Engineering) Bangalore

Conference website: <http://ieee-cas-bangalore.org/wintechcon/>

Online registration: <https://in.explara.com/e/ieee-wintechcon-2019>



ADVANCE PROGRAM

7:45 am - 8:45 am	Registration			
8:45 am - 9:00 am	Inauguration and Welcome Address			
9:00 am - 9:30 am	Keynote I: Reena Dayal Yadav, Director, Microsoft Garage-India			
9:30 am - 10:00 am	Coffee Break			
	Track 1.1: VLSI Design and Test-1	Track 2.1: System Optimization-1	Track 3.1: System Software and AI-1	WinTechCon Hackathon for a Smart, Safe & Sustainable World
Session 1: 10:00 am - 11:00 am	<i>Shweta Aladakatti, Jasdeep Jain, Shubham Singh, Narayanan Natarajan, Purna Nayak and Yojak Raote – “Optimized Battery Life for Ultra-Low Power Products” (10:00 am – 10:20 am)</i>	<i>Ruchi Shankar, Shalini Eswaran, Sharavathi Bhat and Lakshmanan Balasubramanian – “Novel Pulse Width Insensitive Design and Verification Methods”</i>	<i>Ramya Bagavath Singh, Gaurav Kumar, Priya Ranjan Sinha, Gaurav Sultania, Shashank Shrikant Agashe and Chulmoo Kang – “Automatic Defect Classification and Localization of MURA Defects”</i>	Talent Hackathon – HW & SW – Building on New Ideas to Reality
	<i>Nandhini Rajaiah, Jayakumar N Sankarannair and Larry S Leitner – “Validation Methodology</i>	<i>Vandana Singh, Vani V, Ranjith Kulai and Gregory Matthew James – “Novel Infrastructure Design for</i>	<i>Shalini Govil, Rohit Kumar and Pavan Kumar Devarayanigari – “Uplink Data Compression for</i>	

	<i>for Nest Memory Management Unit” (10:20 am – 10:40 am)</i>	<i>Multi-FPGA Prototyping system”</i>	<i>Futuristic Wireless Networks”</i>	
	<i>Ruchi Shankar, Prachi Mishra, Abhinav Parashar, Ashwini Padoor and Lakshmanan Balasubramanian – “Design and Verification Challenges with Third Party IP Cores” (10:40 am – 11 am)</i>	<i>Aishwarya Bhatnagar, Vaibhavi Shanbhag and Navaneeth Kumar – “Integrating Functional Safety in AC/Servo Drives with Redundancy and Diagnostic Coverage”</i>	<i>Bakshree Mishra, Dipam Chakraborty, Srajudheen Makkadayil, Saurabh Patil and Bhaskar Nallani – “Edge Acceleration of Computer Vision and Deep Learning Algorithms using OpenCL”</i>	
	Track 1.2: VLSI Design and Test-2	Track 2.2: System Optimization-2	Track 3.2: System Software and AI-2	
Session 2: 11:00 am - 12:00 pm	<i>Chaithanya Kolikipudi, Sreenivas Machavaram, Venkataraghavan Parakalan and Milea Tal – “Automation of Verification for Application Specific Instruction Set Processors” (11:00 am – 11:20 am)</i>	<i>Krishna Daamini Ellendula, Vinay Kumar Shrivastava, Lalit Kumar Pathak and Rohan Raj – “Cross-Layer Optimized MBMS Performance for DSDS User Equipment”</i>	<i>Ruchi Kansara, Jitendra Otwani, Niladri Paria and Dr. Sajal Das – “An Optimal Resource Sharing Algorithm for Multi-Sim UE with Single RF Subsystem”</i>	
	<i>Ruchita Pathak, Vishal Kumar Dewan and Aravind Bhat – “HVDM Solution for Random Verification of Microcontroller based Subsystems” (11:20 am – 11:40 am)</i>	<i>Lakshmi Prasanna Jasti, Rohit Kumar, Tushar Vrind and Lalit Pathak – “Novel Approach for Prioritization of TCP Acknowledgements Beyond 4G and 5G Network”</i>	<i>Sushma Vishwakarma, Anantha Sharma and Sharath Kodase – “Enhancing eMMC with Multi-Stream”</i>	
	<i>G S Aishwarya Meghana, Sheetal Y Kochrekar and Poornima Venkatasubramanian – “A Generic Layout Automation Model to Achieve Minimal Time-to-Market” (11:40 am – 12 pm)</i>	<i>Divya U – “Kernelization of Vertex Cover Based on Crown Structure”</i>	<i>Purna Chandra Mandal, Nayana Mariyappa, Souryendu Das and Anbuvelu Venkataraman – “Storage Offload on SmartNICs”</i>	
12:00 pm - 1:15 pm	Lunch & Learn: Poster and Demo Sessions			
1:15 pm - 1:45 pm	Keynote II: Dr. Geetha Manjunath, Co-founder & CEO, Niramai “AI-enabled Healthcare for the Masses”			Hackathon Evaluation
1:45 pm - 3:00 pm	Tutorial 1: Autonomous Driving and Advancements	Tutorial 2: AI Model Testing – Are We There Yet?	Tutorial 3: Emerging Trends in 5G and WiFi	
3:00 pm - 3:45 pm	Coffee & Learn: Poster and Demo Sessions			
3:45 pm - 4:00 pm	Hackathon Result Announcement & Awards			
4:00 pm - 4:45 pm	Panel Discussion: Breaking Stereotypes – Perspectives from a Woman’s Technology Career			
5:00 pm - 5:45 pm	Awards & Closing Ceremony			